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ARCHITECTURE FOR CONNECTION AND AGGREGATION COMPONENTS WITHIN A RACK

ABSTRACT OF THE DISCLOSURE

The present invention generally is directed to an apparatus for holding one or more devices, comprising a rack. The rack includes one or more devices in which at least one device has a microcontroller. The rack further includes a device memory associated with each device containing a unique identifier. The microcontroller is connected to the device memory through a bus. The microcontroller is configured to retrieve the unique identifier.